Title: Semiconductor collaboration

Name: Astha srivastava

Abstract:
SCL has been designing and developing ASICs and CCDs for meeting requirements of the Country. Out of the ASICs and CCDs developed by SCL, 4K Linear Imager and Clock Driver have been used in Indian Mini Satellite (IMS) for remote sensing application. SCL has been regularly providing, for over a decade, a set of nine ASICs for control electronics of 3-Phase Electric Locomotive of Indian Railways (IR). SCL will, in future also, continue to provide these nine ASICs to IR for the Locomotive.

SCL has been part of many international collaborations and have provided ASICs for applications therein. Major one is development and delivery of required quantity of Multiplexed Analog Signal Processor (MANAS) ASIC for an International Experiment at CERN-Switzerland. In future, SCL's participation in such collaborations will continue for development and delivery of ASICs for required applications.

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Biography



Miss Astha Srivastava is a bachelor degree holder of Indian origin. She completed her B.Tech in Metallurgy Science from CSJMu (Chhatrapati Shahu Ji Maharaj University)

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