2nd Global Summit on

Electronics and Electrical Engineering conferenceseries.com

November 07-08, 2016, Las Vegas, Nevada, USA

Organizing Committee Members



Abdallah Ougazzaden
USA



Markus Zahn
USA



Cardinal Warde
USA



Kenneth R. Laker
USA



Aggelos K.Katsaggelos
USA



Wu Lu
USA

Program at a Glance

Day 1

Morning Sessions

Reception/Registration	08.00-09.00
Time	**General Session**
09.00-09.15	Inaugural Address
09.15-09.45	Keynote/Plenary Talk 1
09.45-10.15	Keynote/Plenary Talk 2
Keynote Slots are Available	
Panel Discussions/Group Photo	
Coffee/Tea Break 10.45-11.00 (Networking)	
11.00-12.40	5 Speakers (20 Mins Each)
Speaker Slots are Available	
Lunch Break 12.40-13.30	
13.30-15.30	6 Speakers (20 Mins Each)
Speaker Slots are Available	
Coffee/Tea Break 15.30-15.45 (Networking)	
15.45-17.25	5 Speakers (20 Mins Each)
Speaker Slots are Available	

19 Speakers for **Day 1**

Day 2

Morning Sessions / Evening Sessions

	Time	Session 1	Session 2
09.00-10.40		5 Speakers (20 Mins Each)	5 Speakers (20 Mins Each)
Speaker Slots are Available			
Coffee/Tea Break 10.40-10.55 (Networking)			
10.55-12.35		5 Speakers (20 Mins Each)	5 Speakers (20 Mins Each)
Speaker Slots are Available			
Lunch Break 12.35-13.25			
13.25-15.05		**5 Speakers (20 Mins Each)**	**5 Speakers (20 Mins Each)**
Poster Sessions			
Coffee/Tea Break 15.05-15.20 (Networking)			
15.20-17.00		**5 Speakers (20 Mins Each)**	**5 Speakers (20 Mins Each)**

20 Speakers on **Day 2**

NOTE: Program Shedule is subject to change with final allotment of the speaker slots

Awards & Closing Ceremony

Tourist Atrractions

conferenceseries.com









